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Serial No.	Filing Date	Examiner	Group Art Unit
<u>10/706,228</u>	<u>November 12, 2003</u>	<u>Vuthe Siek</u>	<u>2825</u>

Invention:

DATABASE MINING SYSTEM AND METHOD FOR COVERAGE ANALYSIS OF
FUNCTIONAL VERIFICATION OF INTEGRATED CIRCUIT DESIGNS

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PTO/SB/123 (04-05)

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Patent Number	US 7,007,251 B2
Issue Date	February 28, 2006
Application Number	10/708,228
Filing Date	November 12, 2003
First Named Inventor	Hekmatpour
Attorney Docket Number	RPS920030129US1 (IRA-10-5790)

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